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Calls. We Will Describe The Simulator And The Wrap-per Library In The Next Two Sub-sections, And F Mar 24th, 2024

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Simulation Defined Logic Simulation

4 Fault Simulation Scenario Circuit Model: Mixed-level Mostly Logic With Some Switch-level For High- Impedance (Z) And Bidirectional Signals High-level Models

(memory, Etc.) With Pin Faults Signal States: Logic Two (0, 1) Or Three (0, 1, X) States For Purely Boolean Logic Circuits Four States (0, 1, X Jan 9th, 2024

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ANALYSIS FOR THE DESIGN OF SIMULATION EXPERIMENTS

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