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Chapter 8 Memory Hierarchy And Cache Memory

• Suppose Processor Has 2 Levels Of Hierarchy: Cache And Main Memory • $T_{Cache} = 1$ Cycle, $T_{MM} = 100$ Cycles • What Is The AMAT Of The Program From Example 1? $AMAT = T_{Cache} + MR_{Cache} (t_{MM}) = [1 + 0.375(100)]$ Cycles = 38.5 Cycles
Memory Performance Example 2 1th, 2024

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7-2 Chapter 7- Memory System Design Computer Systems Design And Architecture By V. Heuring And H. Jordan © 1997 V. Heuring And H. Jordan: Updated David M. Zar ... 1th, 2024

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