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Co-Design Of Deep Neural Nets And Neural Net Accelerators For Embedded Vision Applications Kiseok Kwon,1,2 Alon Amid,1 Amir Gholami,1 Bichen Wu,1 Krste Asanovic,1 Kurt Keutzer1 1 Berkeley Al Research, University Of California, Berkeley 2 Samsung Research, Samsung Electronics, Seoul, South Korea {kiseo Feb 8th, 2024

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Neural Network, Power, Inference, Domain Specific Architecture ACM Reference Format: KiseokKwon,1,2 AlonAmid,1 AmirGholami,1 BichenWu,1 KrsteAsanovic,1

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