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## **MICROPROCESSORS AND MICROCONTROLLERS**

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## **MICROPROCESSORS AND MICROCONTROLLERS**

### **MODULE 1 THE X86 ...**

MP, CSE, VCET 3 » The Intel 8086/8088 » 16-bit Pipelined Microprocessors » Addressed 1M Bytes (1M Byte = 1024K Bytes =  $1024 * 1024$  Bytes = 1,048,576 Bytes) Of Memory » Executed 2.5 MIPs (millions Of Instructions Per Second) » Its Instruction Set Contained Over 20,000 Instructions » A Small 6- Or 4-byte Instruction Cache Or 1th, 2024

## **MICROPROCESSORS AND MICROCONTROLLERS 3**

### **0 0 3 OBJECTIVE ...**

Microprocessor/ Microcontroller Based System For A Given Control Application. ... “Microcomputer Systems: The 8086 / 8088 Family - Architecture, Programming And Design”, Second Edition, Prentice Hall Of India, 2007. 3. A. K. Ray & K. M. Bhurchandi, “Advanced

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2024

## **ECB3103 MICROPROCESSORS & MICROCONTROLLERS ...**

1. Yu-Cheng Liu, Glenn A. Gibson, "Microcomputer Systems: The 8086 / 8088 Family – Architecture, Programming And Design", Second Edition, Prentice Hall Of India, 2007. 2. Kenneth J Ayala, "The 8051 Microcontroller Architecture Programming And Application", 2nd Edition, Pen 2th, 2024

### **Microprocessors, Lecture 5: AVR Microcontrollers -Timers**

University Of Tehran 37 Accessing 16-bit Registers In AVR •  $TCNT1 = 0x05ff$ , We Want To Save The Content Of  $TCNT1$  In  $R20$  And  $R21$  • Cannot Read  $TCNT$  In One Cycle – AVR Is A 8-bit Machine • Read  $TCNT1L$  ( $0xff$ ) At  $T0$ , At The Same Cycle Occurs  $TCNT = 0x0600$  • Read  $TCNT1H$  ( $0x06$ ) • The Content 1th, 2024

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3. While Coming To The Lab Bring The Lab Manual Cum Observation Book, Record Etc. 4. Take Only The Lab Manual, Calculator (if Needed) And A Pen Or Pencil To The Work Area. 5. Before Coming To The Lab, Prepare The Prelab Questions. Read Through The Lab Experiment To Familiari 3th, 2024

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Working Steps For C Interrupt Programming Include  
Interrupt File #include Sei ( ) & Cli ( ) Instructions For I  
Flag. Define ISR ISR (interrupt Vector Name) { // Our  
Program } For Interrupt Vector Name We Must Use ISR  
Name From T 3th, 2024

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Features Of The ALS-SDA-86 8086 MEL Microprocessor  
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Battery Backup Facility For RAM. 48 Programmable I/O  
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Demonstration Programs For 8086 Trainer Kit 21.  
Addition Of Two 16-bit Numbers Using 8086 Trainer Kit  
22. Addition Of Two 16-bit Numbers With Respective  
To Address Using 8086 Trainer Kit 23. Addition Of Two  
Multi Byte Numbers Using 8086 Trainer Kit 24.  
Subtraction Of Two 16-bit Numbers Using 8086 Trainer  
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Of Microcontroller-based Embedded Systems.

Overview Of A Single-chip Microcontroller, H 2th, 2024

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C) What Is The Use Of Bit -Set -Reset Mode Of 8255? [2M] D) What Is The Difference Between A Long Jump (LJMP) And A Short Jump (SJMP) Instruction Of 8051? [3M] E) What Is The Purpose Of Pseudo -instructions? Explain Briefly. [3M] F) Which Ports Of PIC18 Are Bit Addressable? [2M] PART -B 2. A) What Are Microprocessors? 3th, 2024

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