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2/20/2015 6 Microprocessors And Interfacing (A1423)
INTEL 8085 1) Built In 1977 2) 5 In 8085 Means +5 Volt
Supply 3) Initial Clock Speed Of About 3MHz 4) Approx.
6500 Transistors Were Used 5) 8 Bit μ -Processor Using
NMOS Technology SPECIAL FEATURES 1) Extension To
Support New Interrupts :- Maskable (RST 7.5,RST
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- Using In/out Instruction To Store/restore SREG S2, 2008 COMP9032 Week2 8 The Status Register In AVR (cont.) • Bit 7 - I: Global Interrupt Enable - Used To Enable And Disable Interrupts. - 1: Enabled. 0: Disabled. - The I-bit Is Cleared By Hardware After An Interrupt Has Occurred, And Is Set By The RETI Instruction To 2th, 2024

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