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...Condition Mainly Used With Binary Logic Elements Where The Logic State 1
(TRUE) Is Converted To A Logic State 0 (FALSE) Or Vice Versa [IEC 60617-12, IEC
61082-2] 3.20 Logic Inversion Condition Mainly Used With Binary Logic Elements
Where A Higher Physical Level Is Converted To A Lower Physical Level Or Vice Versa
[May 8th, 2024.

KT. BO TRI/ONG, CHU NHIEM THir TRirdNG, PHO CHU NHIEM ...Tuy Nhien, Do
Chuong Trinh Bao Gom Nhieu ITnh Vuc, Lien Quan Den Nhieu Cap Nhieu Nganh.
Cong Tac Phoi Hgp, Hudng Dan, Giam Sat Co Sd Thuc Hien Cac Tieu Chi Theo ITnh
Vuc Chuyen Mon Dugc Ban Chi Dao Huyen Phan Cong Phu Trach Cac Tieu Chi Cua
Mot Sd Nganh Van Chua Thuc Su Vao Cudc, Da Jun 24th, 2024Thir Plenary Nuncil
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