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Client - Operating Systems Windows: 32/64-bit Windows ® 8 (8, 8n, 8 Pro, 8 Pro N),

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16-bit Low Energy Timer • 2× 16-bit Timer/Counter Ultra ...As A Wake-up Interrupt Controller Handling Interrupts Triggered While The CPU Is Asleep. The EFM32 Implementation Of The Cortex-M3 Is Described In Detail In EFM32G Cortex-M3 Reference Manual. 2.1.2 Debug Interface (DBG) This Device Includes Hardware Debug Support Apr 1th, 2024Timer NE/SA/SE555/SE555C - 555 Timer CircuitsTimer NE/SA/SE555/SE555C August 31, 1994 346 853-0036 13721 DESCRIPTION The 555 Monolithic Timing Circuit Is A Highly Stable Controller Capable Of Producing Accurate Time Delays, Or Oscillation. In The Time Delay Mode Of Operation, The

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