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Digital Signal Processing (Kuhn 2005) Analog/digital And Digital/analog Converters, CPU, DSP, ASIC, FPGA Advantages: <sup>3</sup>/<sub>4</sub>noise Is Easy To Control After Initial Quantization <sup>3</sup>/<sub>4</sub>highly Linear (with Limited Dynamic Range) <sup>3</sup>/<sub>4</sub>complex Algorithms Fit Into A Single Chip <sup>3</sup>/<sub>4</sub>flexibility, Parameters Can Be Varied In Software 21th, 2024

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Computer (Figure 1.1(b)). These Represent A Continuous-time, Discrete-time And Digital Signal Respec-tively. As We Know From The Sampling Theorem, The Continuous-time Signal Can Be Reconstructed From Its Sam-ples Taken With A Sampling Rate At Least Twice The Highest Frequency Component In The Signal 20th, 2024

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