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2. Digital Storage Oscilloscope (DSO) 3. Power Supply 4. Patch Cords Procedure: 1. The Connections Are Given As Per The Block Diagram For Natural Sampling 2. Connect The Power Supply In Proper Polarity To The Kit And & Switch It On. 3. Using The Clock Selector Switch Select 8 KHz Sampling Frequency And Using Switch SW2 Select 50% Duty Cycle. Mar 2th, 2024

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ECEN2002 Digital Electronics 3 0 0 3 3 4 ECEN2203 Analog Communication 3 0 0 3 3 5 ECEN2204 Solid State Devices 3 0 0 3 3
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4. G.F.Sudha And B.Nanchariah, "A Distributed Routing Algorithm For Data Gram Traffic In Teledesic System", International Conference On Wireless Communication Networks, SSN College Of Engineering, Chennai, 27-28 June 2003. 5. G.F.Sudha And T.G.Palanivelu, "Visibility Enhancement Of Subsurface Tissue Images", Mar 1th, 2024

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