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3. BLOCK DIAGRAM AND SCHEMATIC DIAGRAM

PDP-433PE, PDP-433PU Driver IC Driver IC IC6501 Buffer IC Driver IC Driver IC Driver IC Driver IC Driver IC Driver IC ADR CONNECT A ASSY AA1 Y4 Y1 Y2 D6 D18 D1 D8 D9 D16 D10 D11 D7 D4 D15 D14 D2 D17 D3 D12 D13 SW21 KL21 KL22 RE1 TE1 Y6 Y3 CLK/LE CLAMP A ASSY CLA1 VADR2 Driver IC Driver IC IC6601 Buffer IC Driver IC Driver IC Driver IC Driver IC ... Jul 4th, 2024

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For Digital To Analog Conversion The Sample Clock Is Usually Derived From An AES Or S/ PDIf Bit Stream. And Like The Analog To Digital Converter, This Regeneration Process Can Introduce Jitter Into The Sample Clock Jan 2th, 2024

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Figure 6. Three-bit, Flash Type Analog To Digital Converter Circuit Analog To Digital Converter (ADC): When Selecting An Analog To Digital Converter (ADC) Chip For An Application, Three Characteristics Must Be Considered. Similar T Jan 1th, 2024

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