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Integrated Circuit Design Assignment 4 Due: Tuesday November 26th, 2013 At 8:00am 1. Consider The Following Circuit. (This Circuit Is Sometimes Referred To As A Self-biased Current Source). R V DD GND M 0 M 2 M 1 M 3 GND 4 GND I Out Assume All Transistors Are Operating In Saturation Region And = =0, V DD =3 V, V TH(NMOS) =0 ... Jan 2th, 2024.

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